

Fig. 1

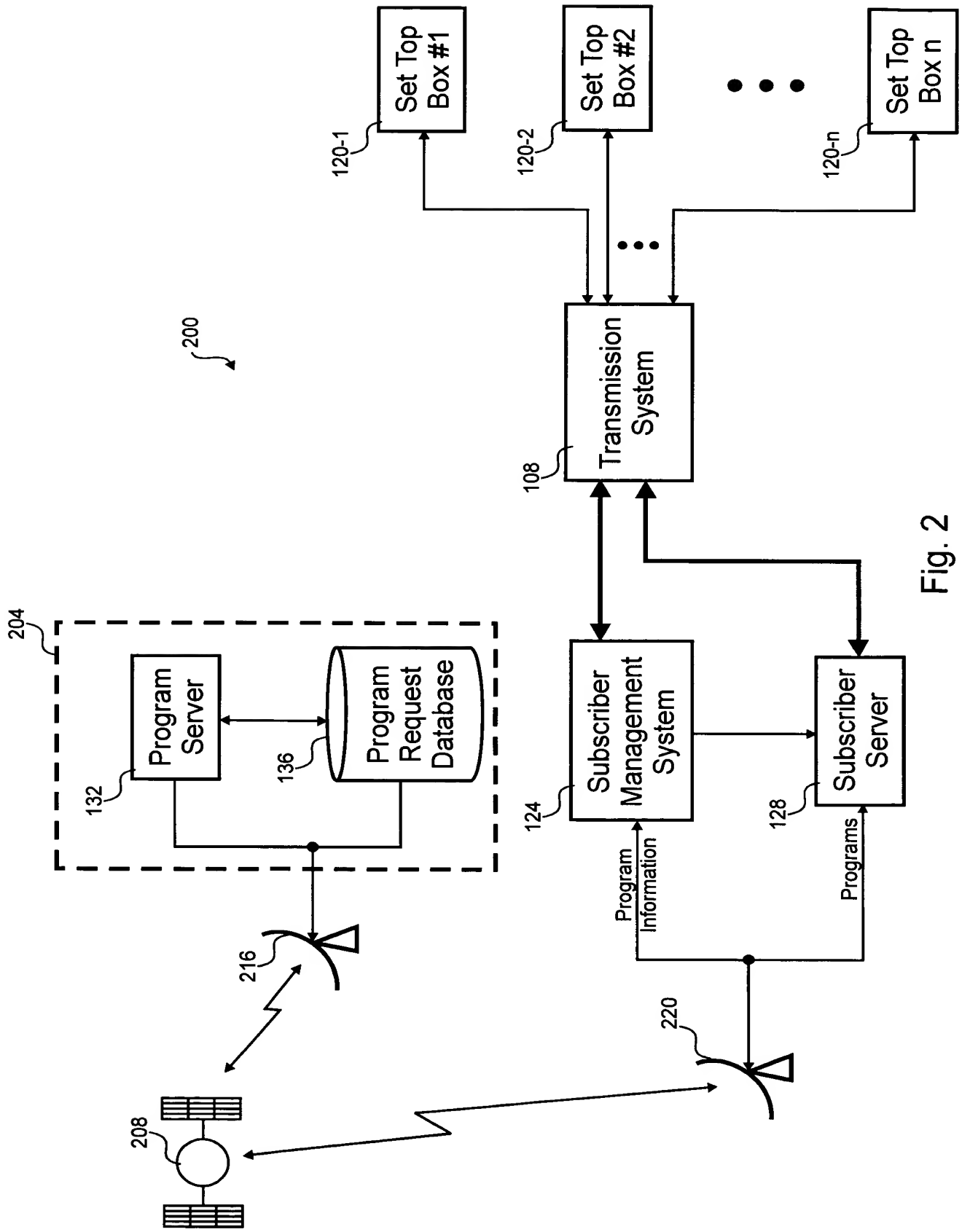


Fig. 2

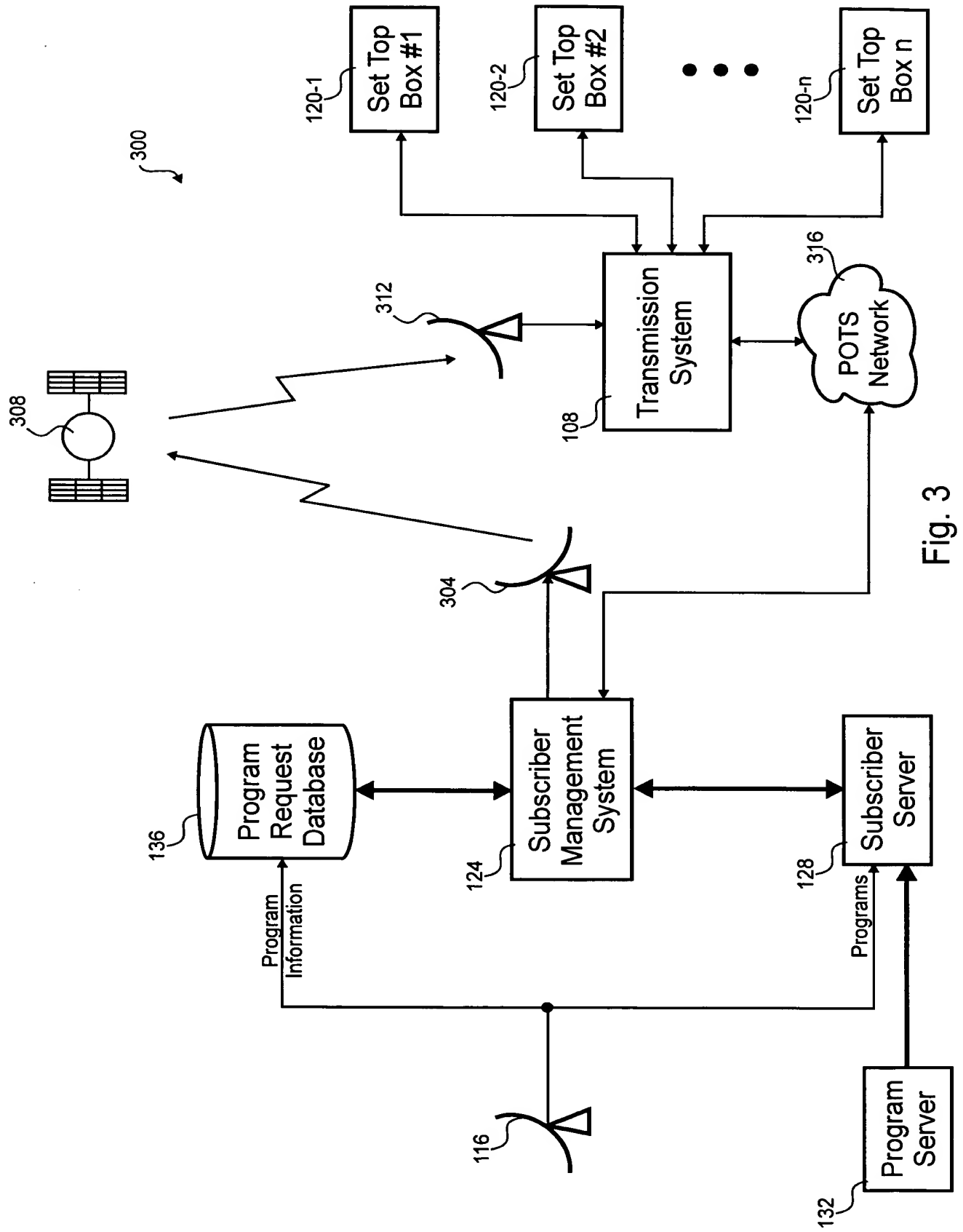


Fig. 3

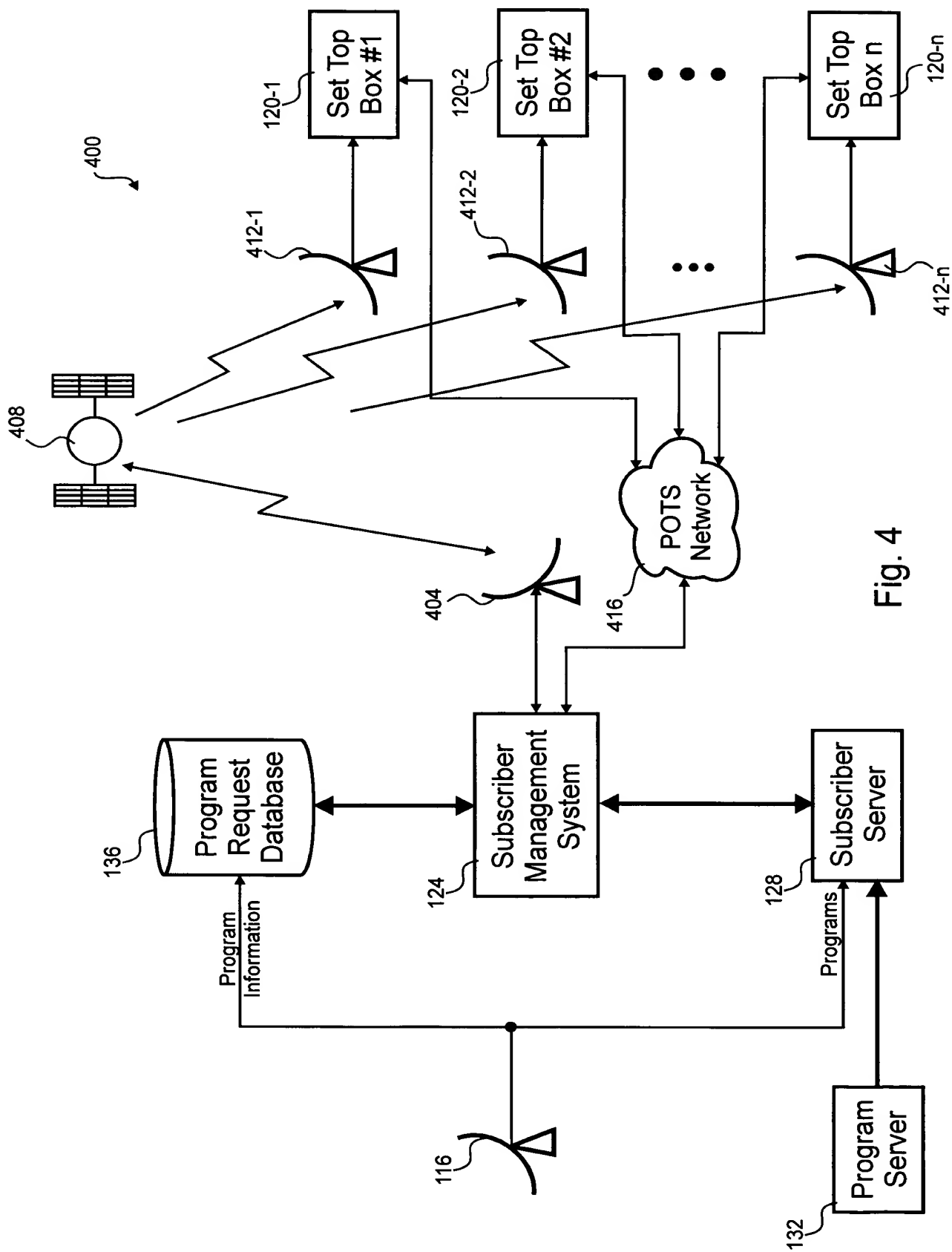


Fig. 4

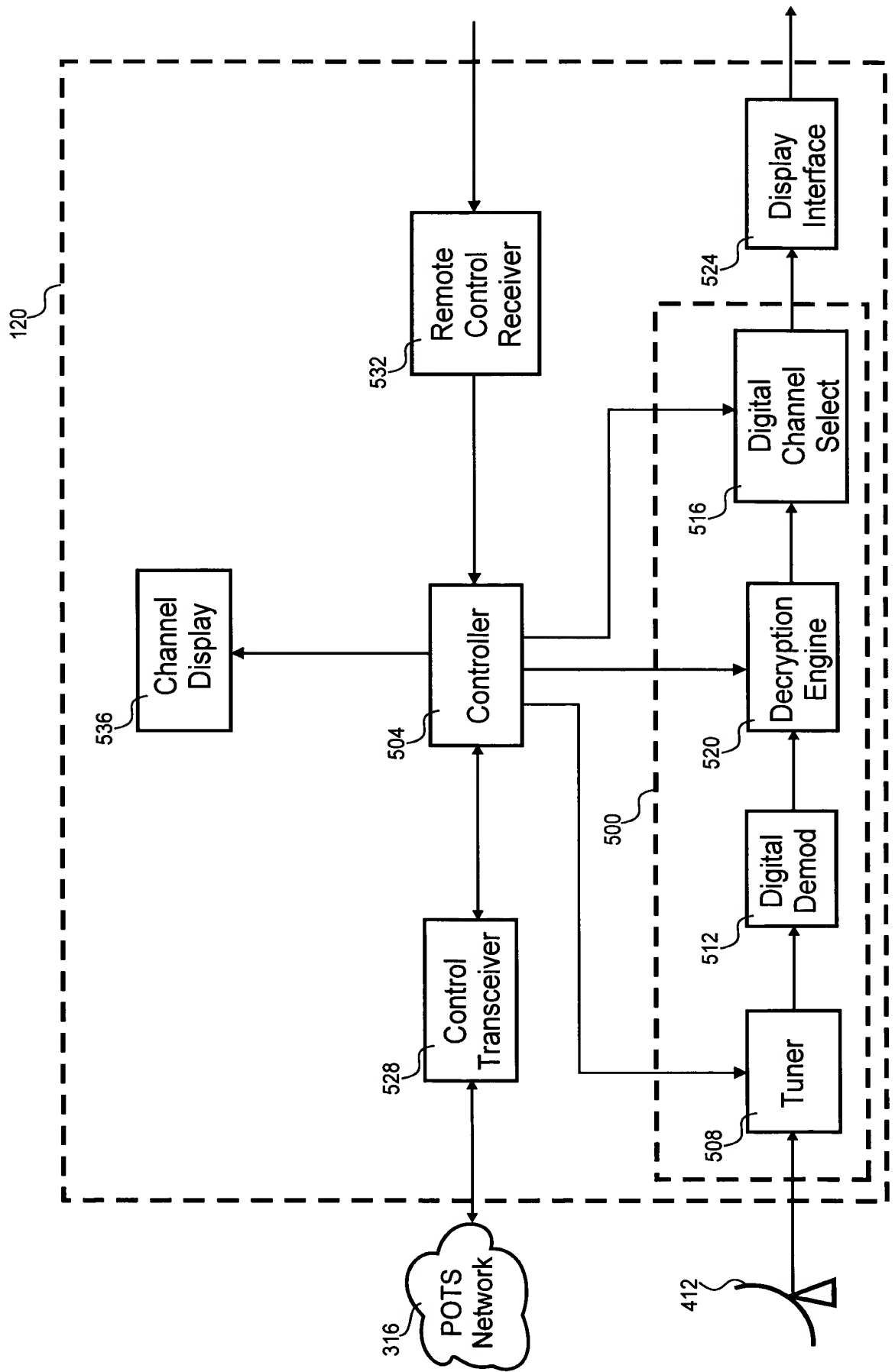


Fig. 5

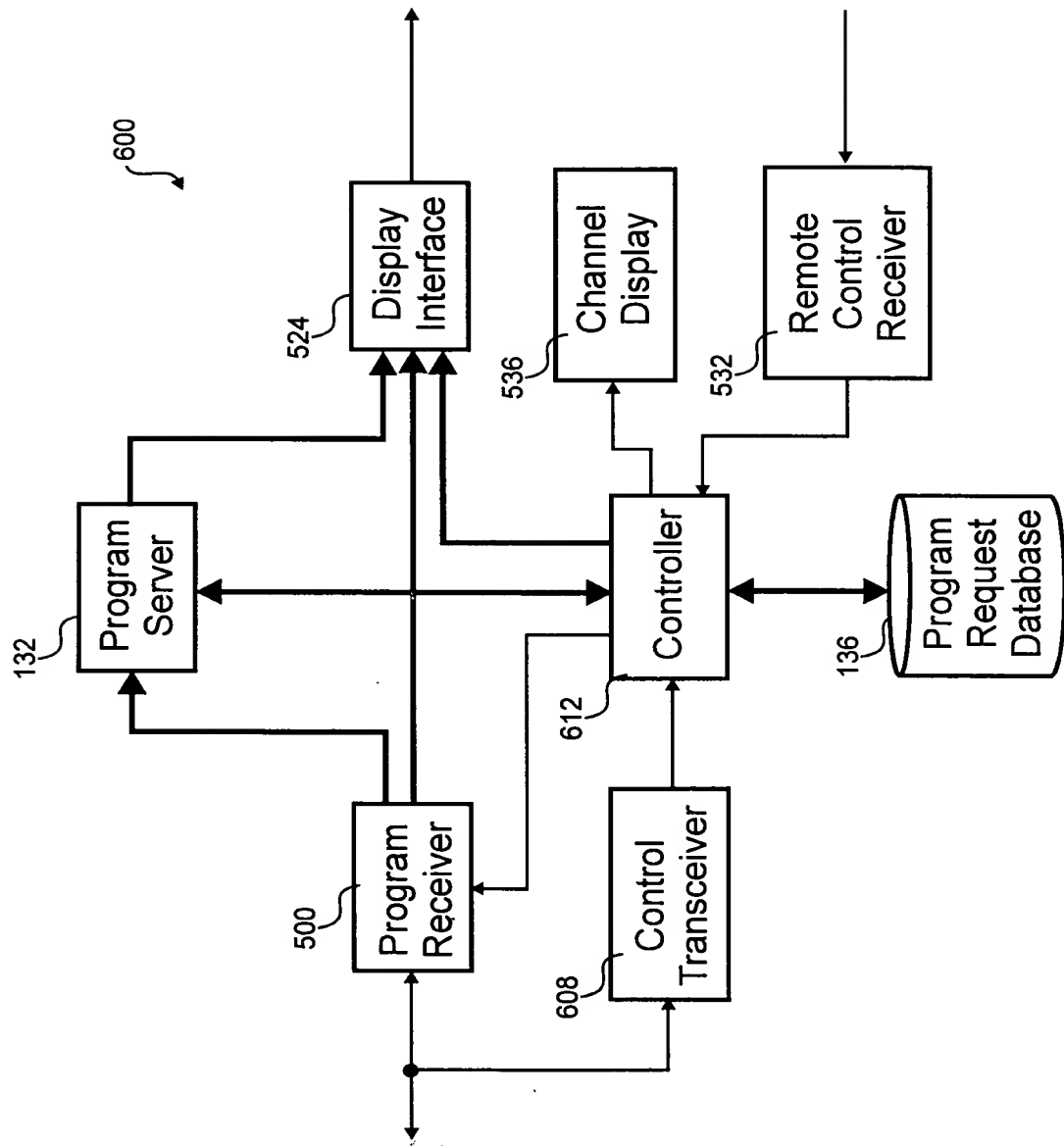


Fig. 6

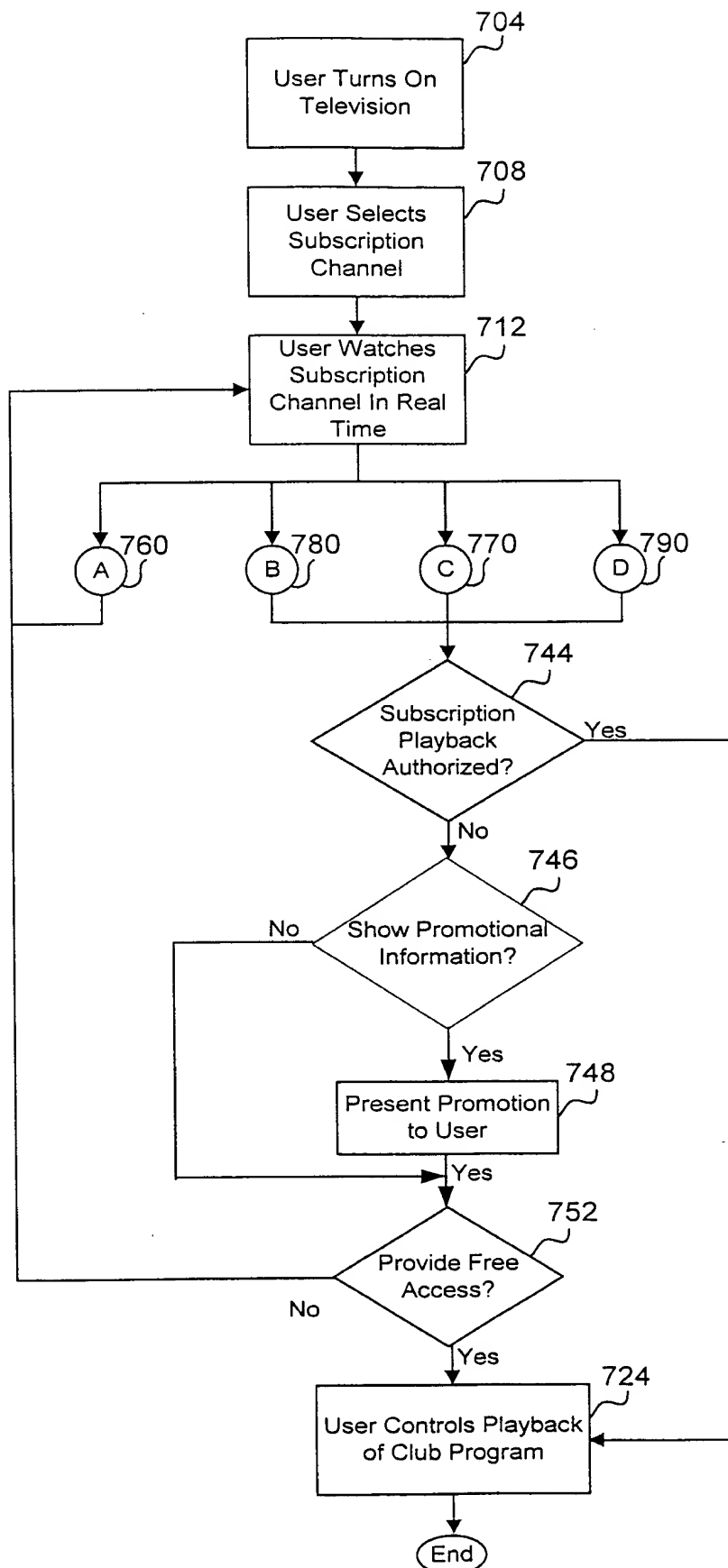


FIG. 7

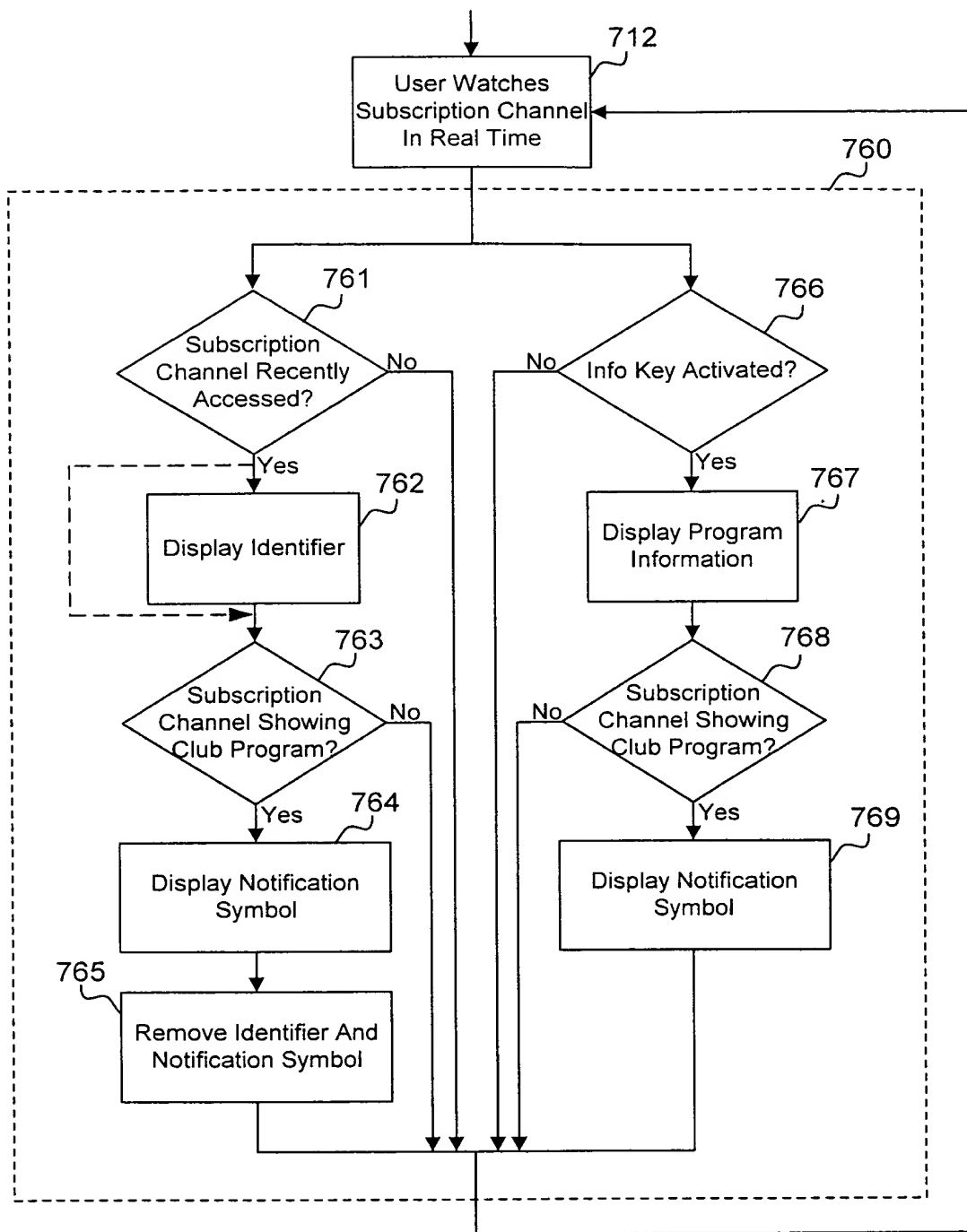


FIG. 7A

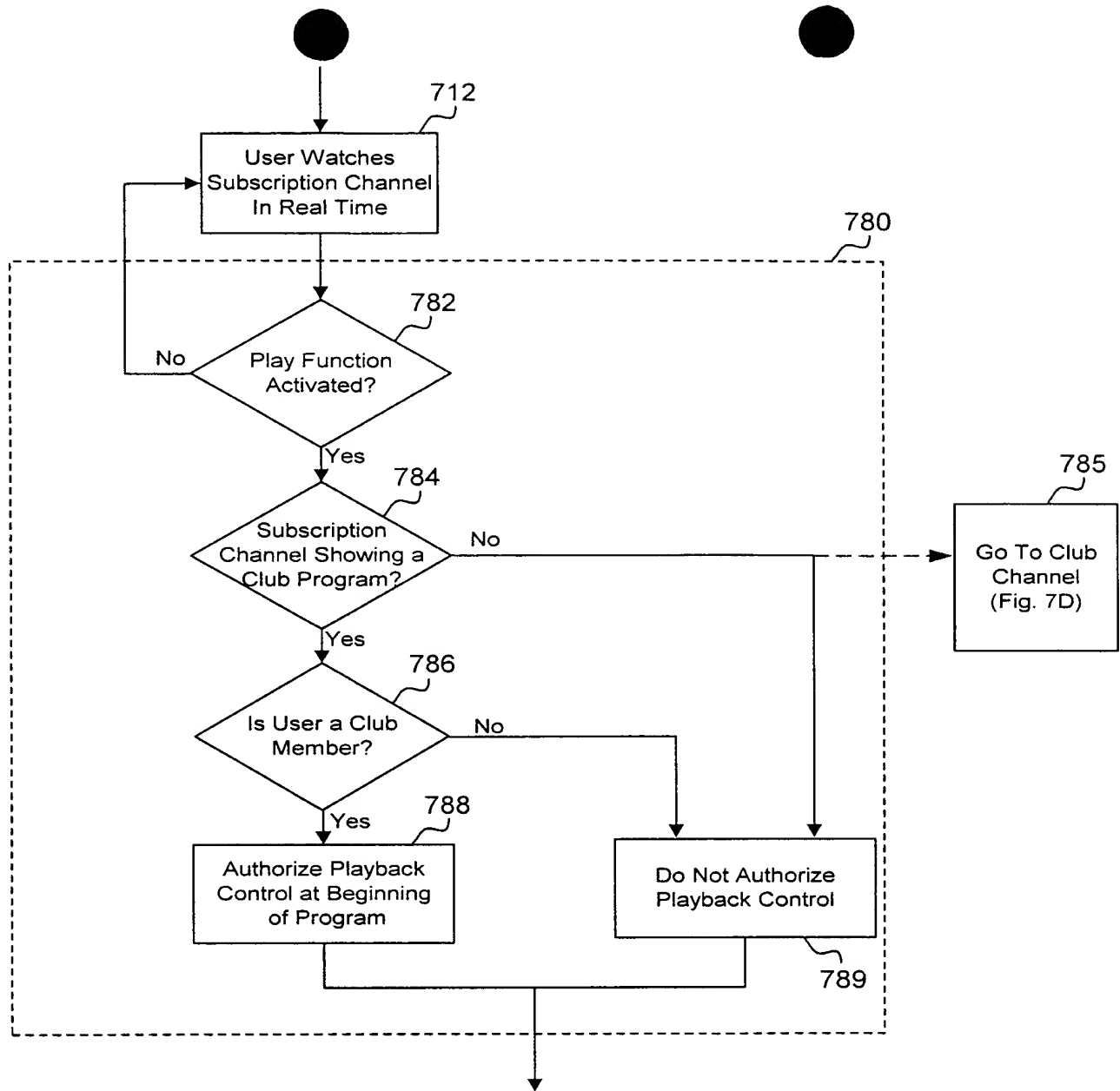


FIG. 7B

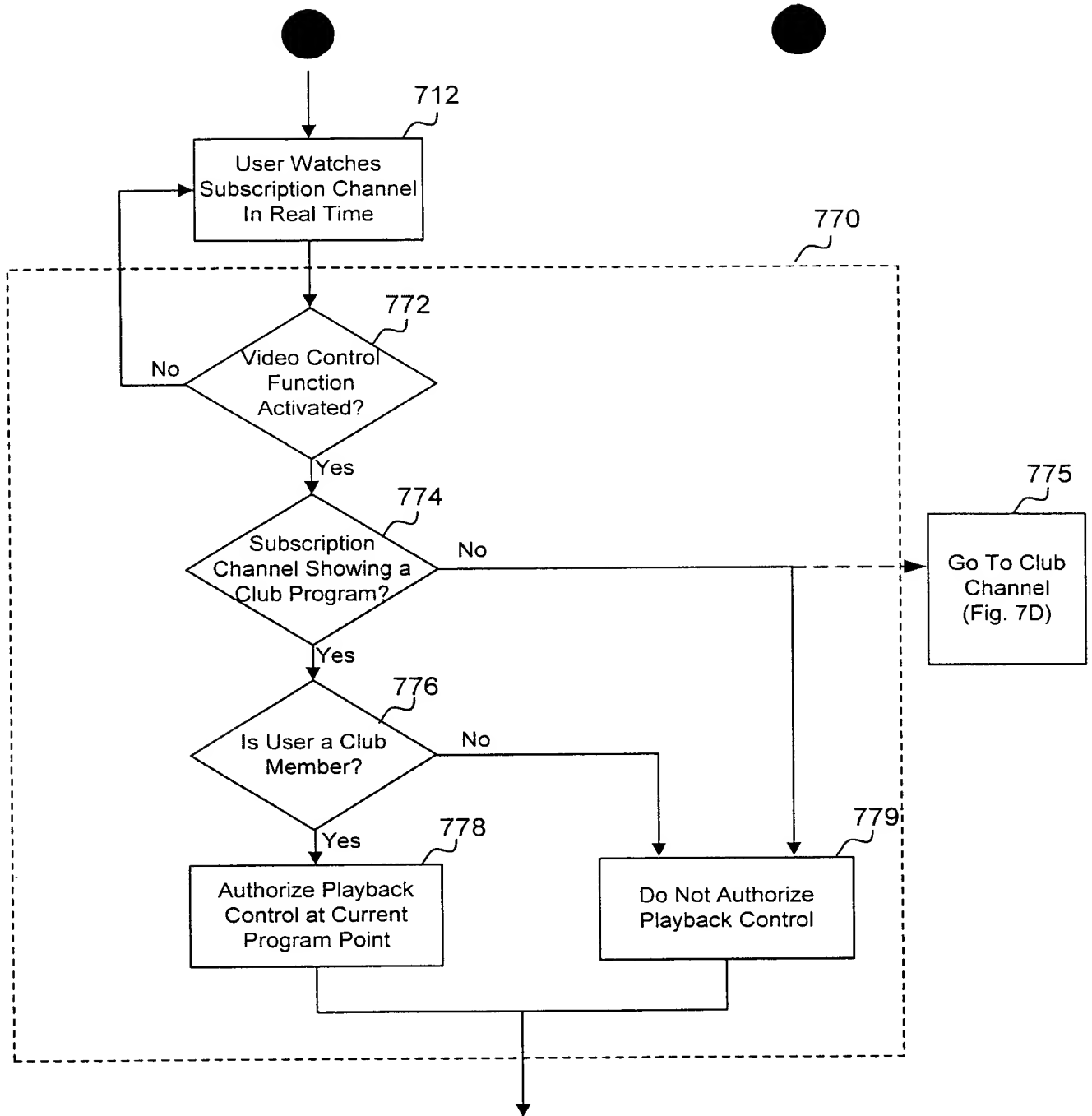


FIG. 7C

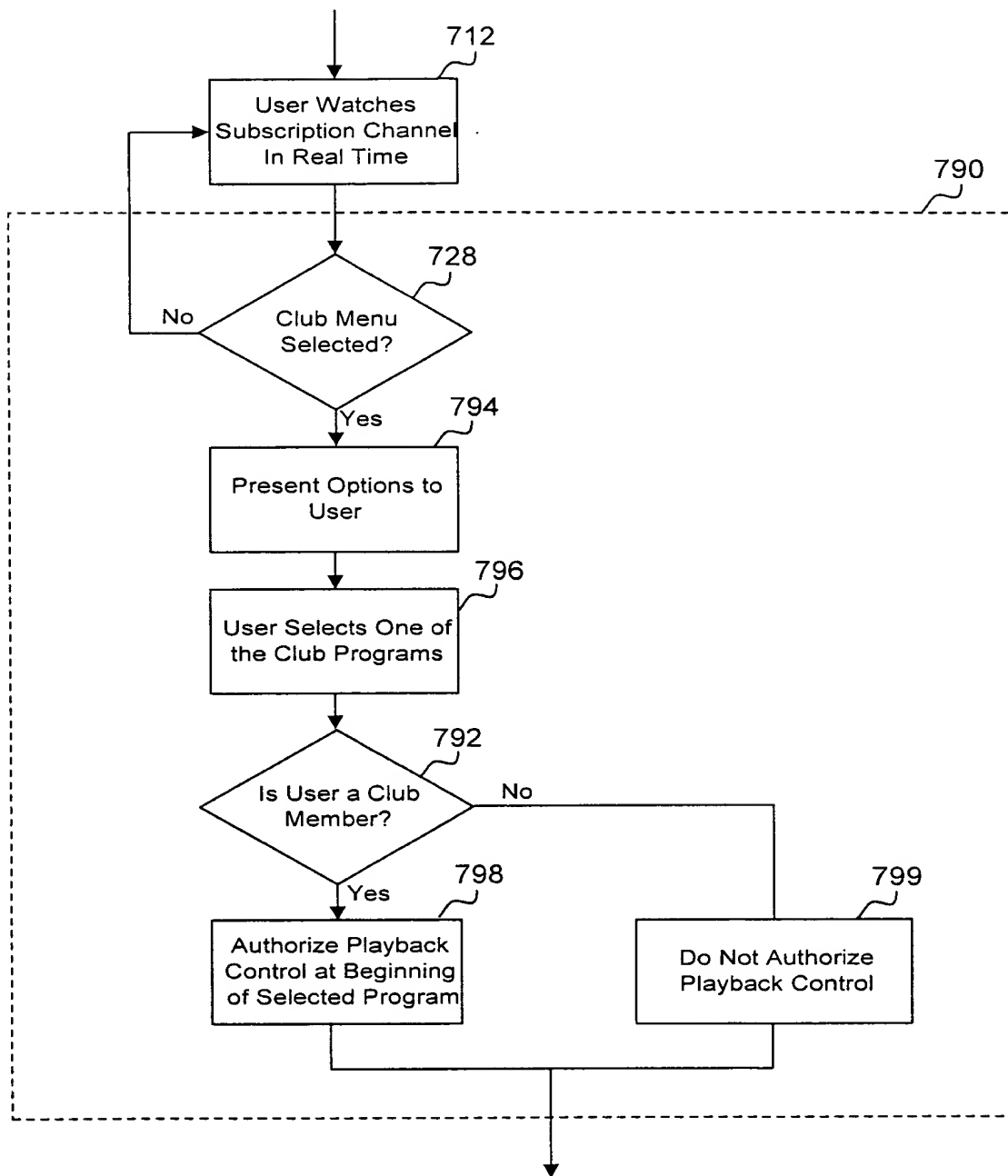


FIG. 7D

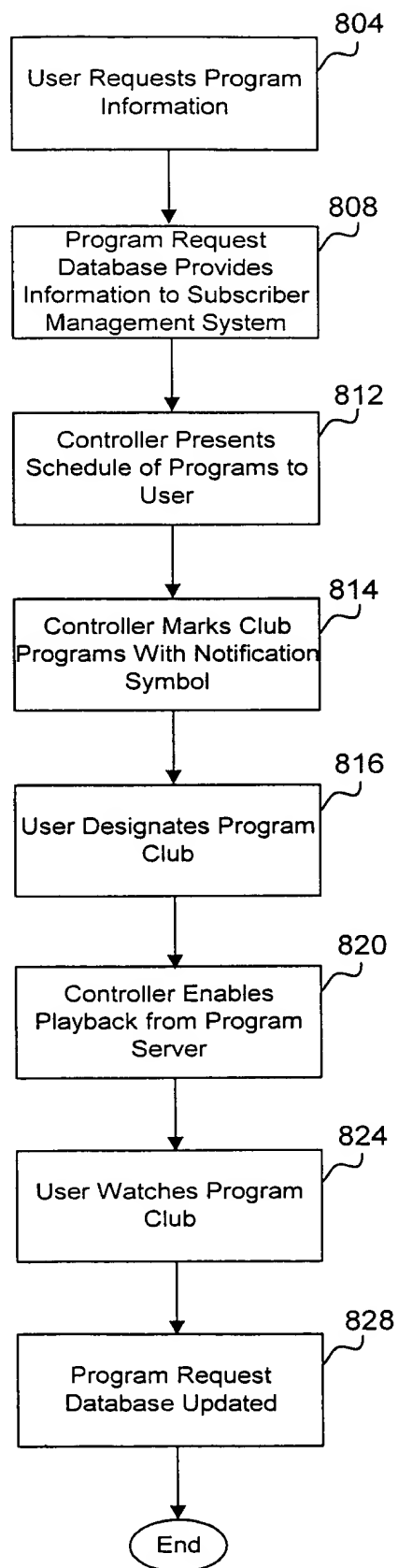


FIG. 8A

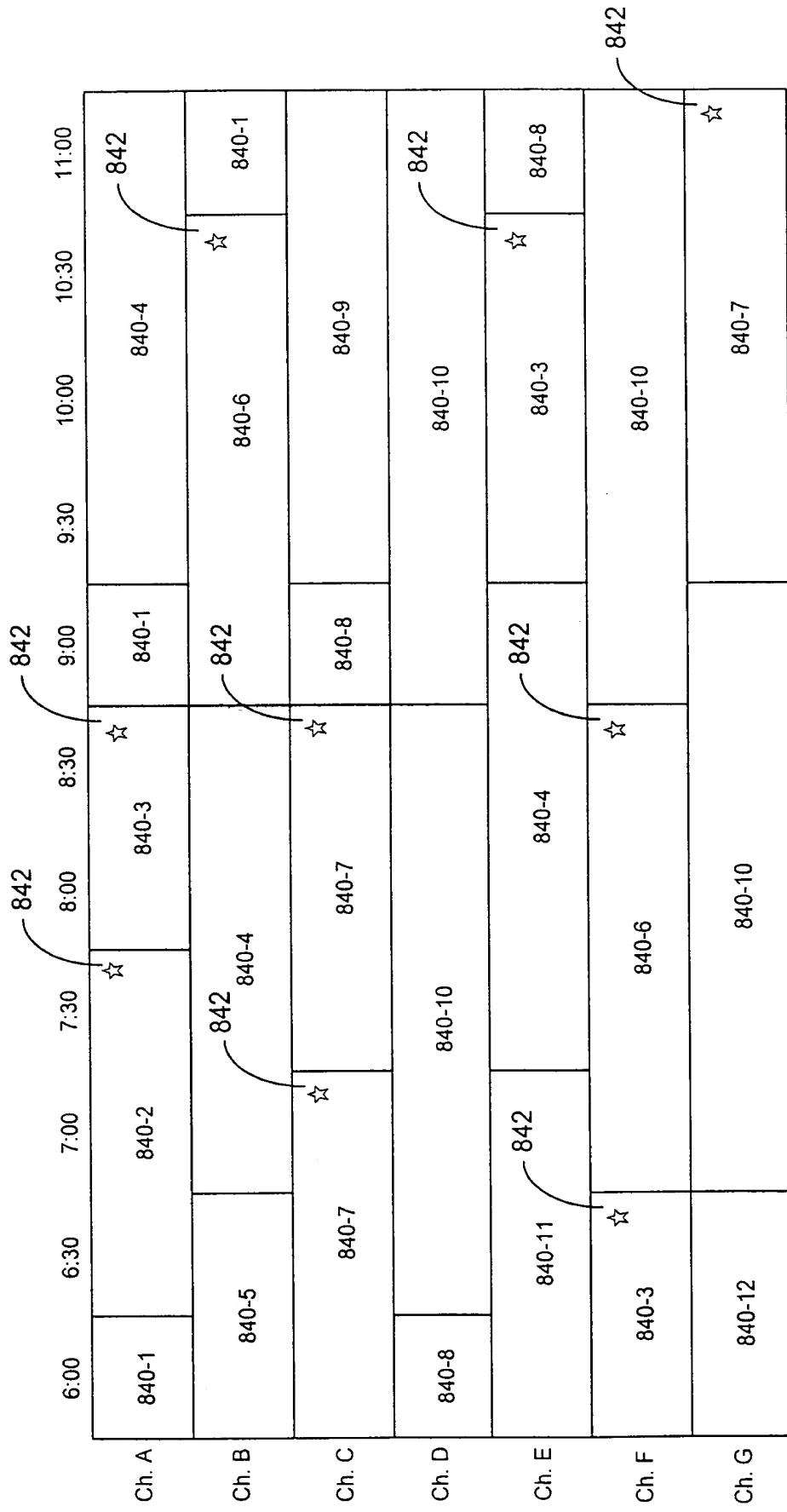


FIG. 8B

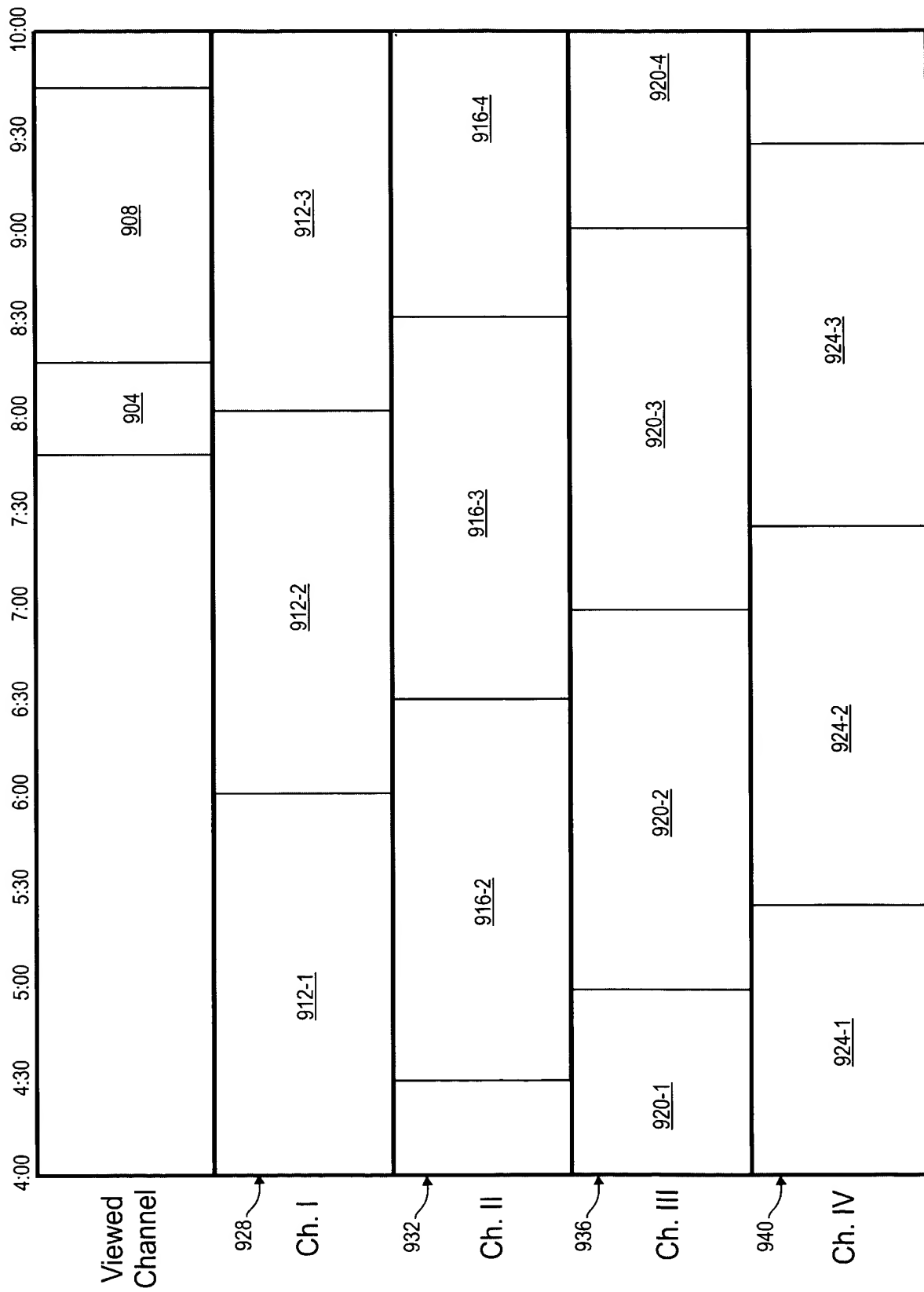


Fig. 9A

FIG. 9B is a diagram of a time slot grid for a multi-channel system. The grid shows time slots for four channels (Ch. I, Ch. II, Ch. III, Ch. IV) over a period from 4:00 to 10:00. The time slots are labeled with channel numbers (960, 964, 968, 972) and are organized into a grid structure. The grid is divided into four main sections, each corresponding to a channel (Ch. I, Ch. II, Ch. III, Ch. IV). The time slots are labeled with channel numbers (960, 964, 968, 972) and are organized into a grid structure. The grid is divided into four main sections, each corresponding to a channel (Ch. I, Ch. II, Ch. III, Ch. IV).

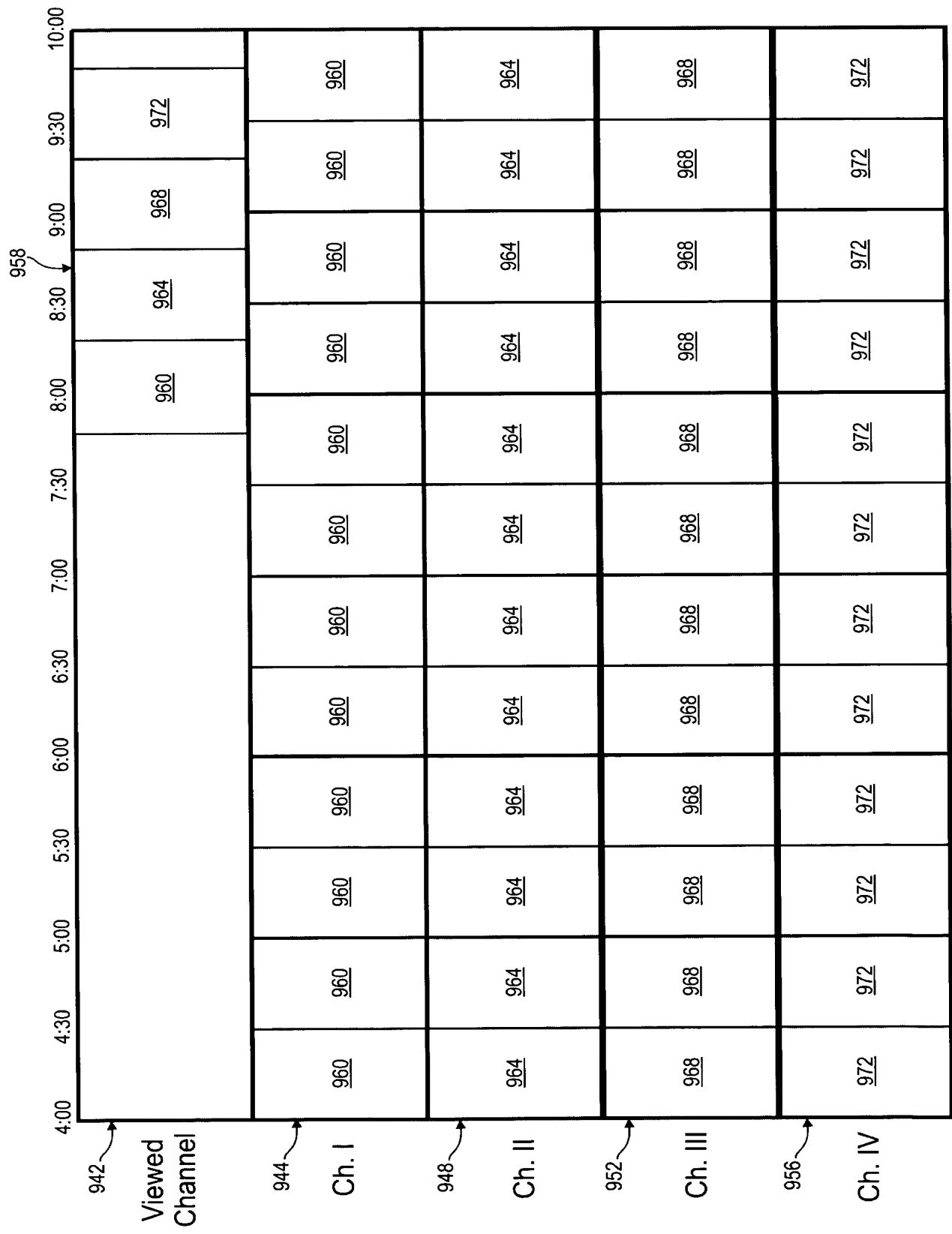


Fig. 9B

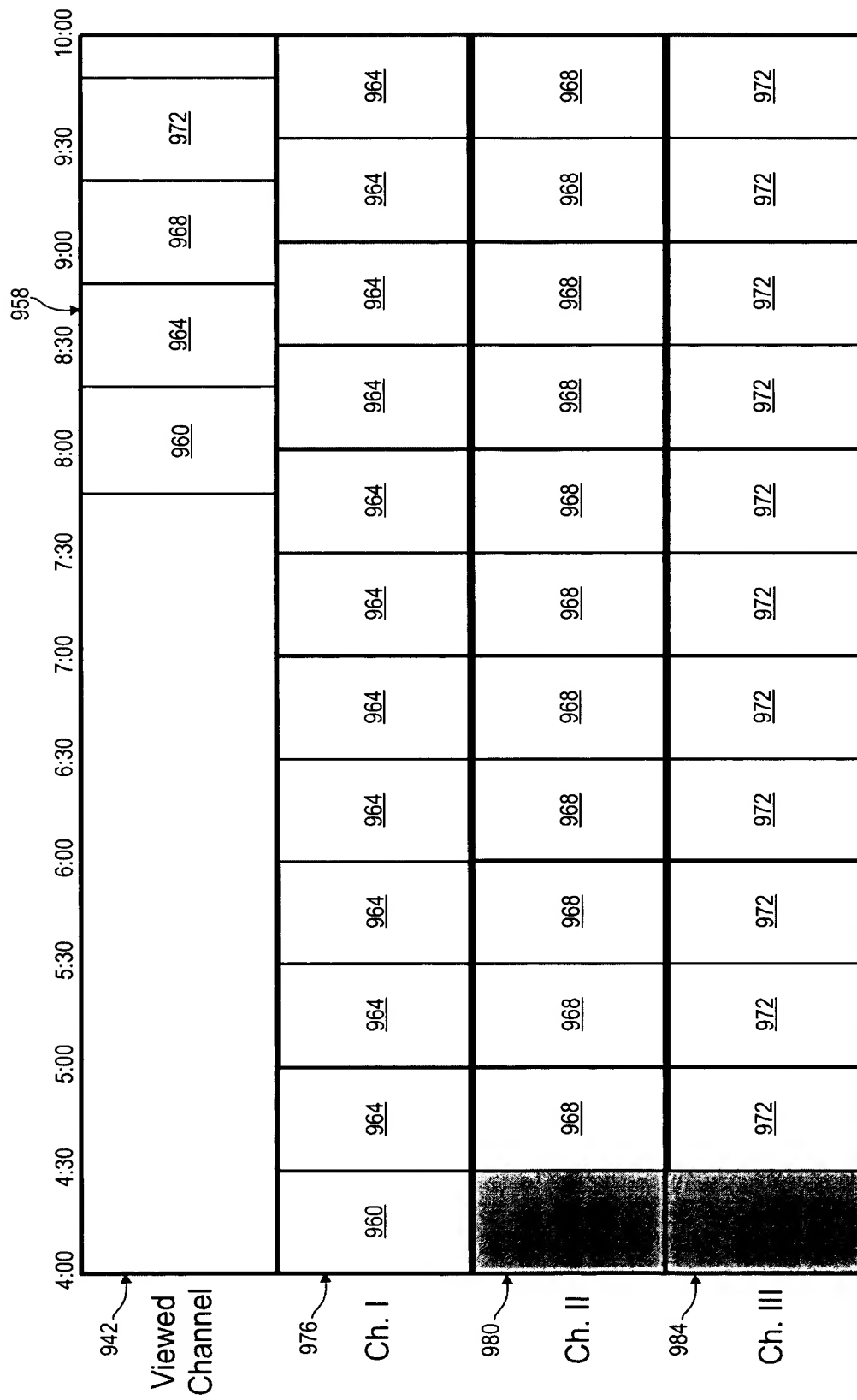


Fig. 9C

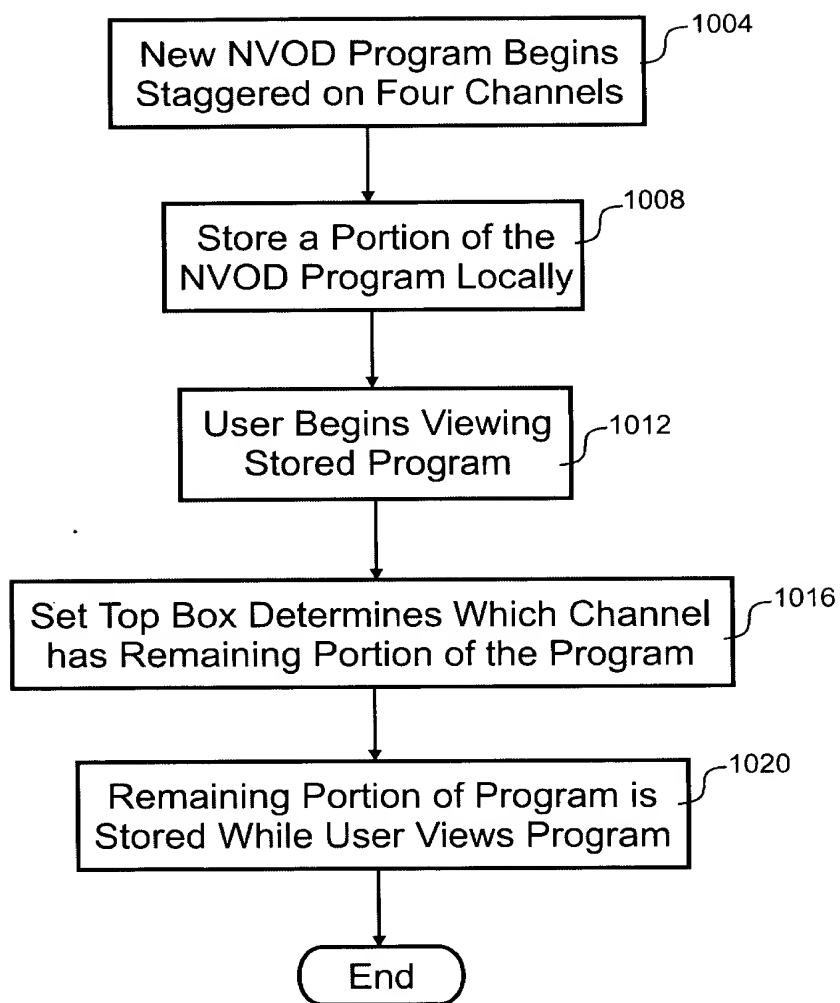


Fig. 10A

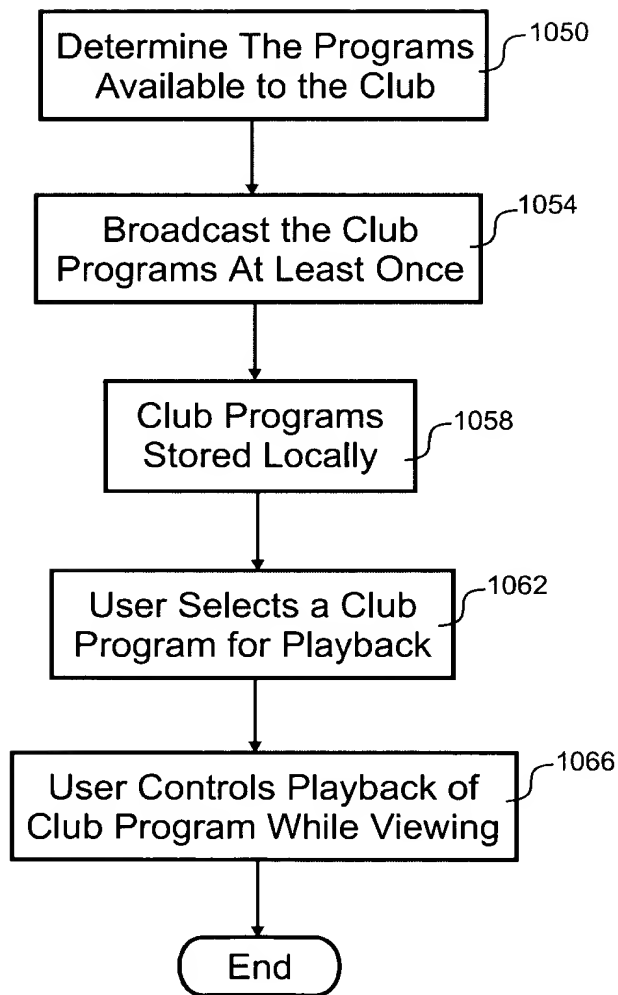


Fig. 10B

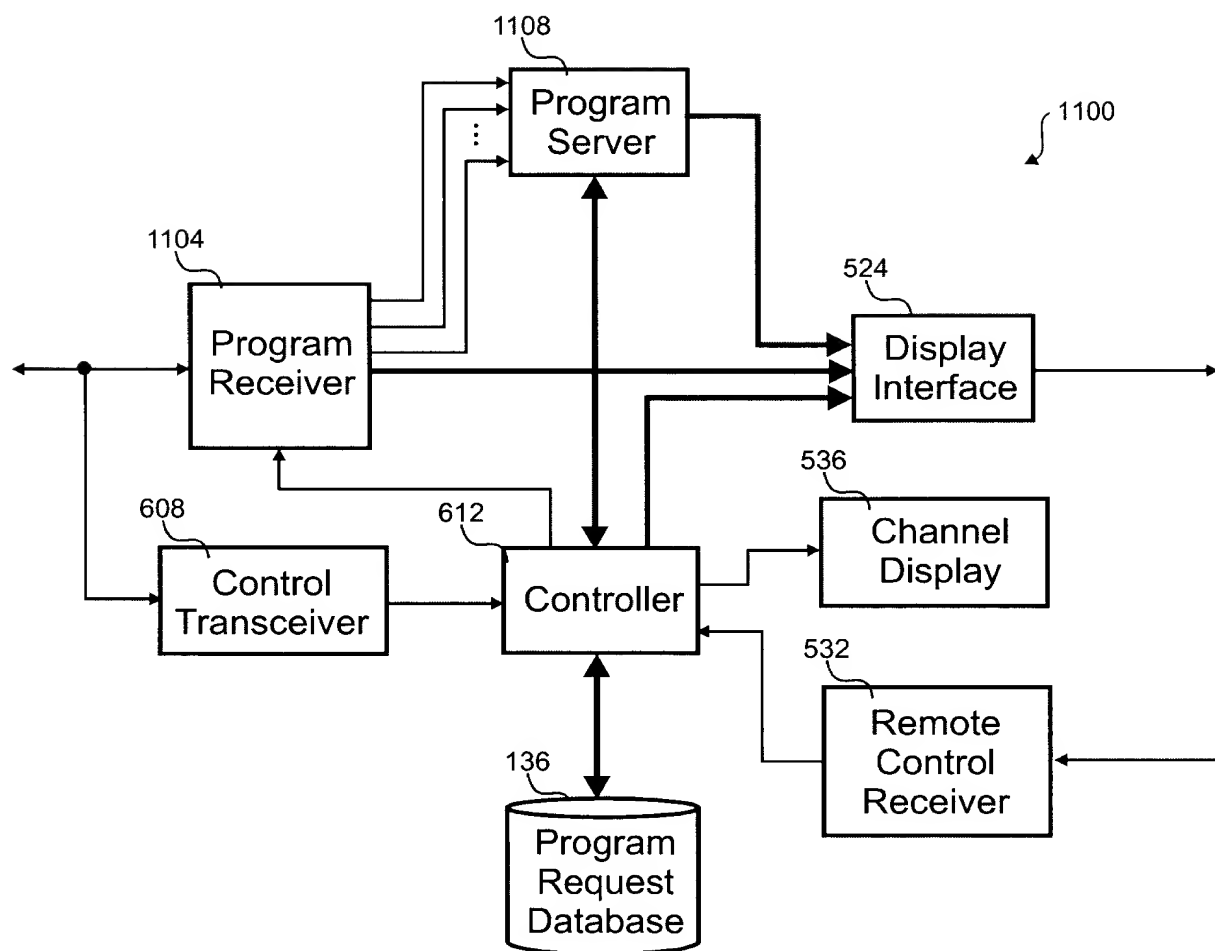


Fig. 11

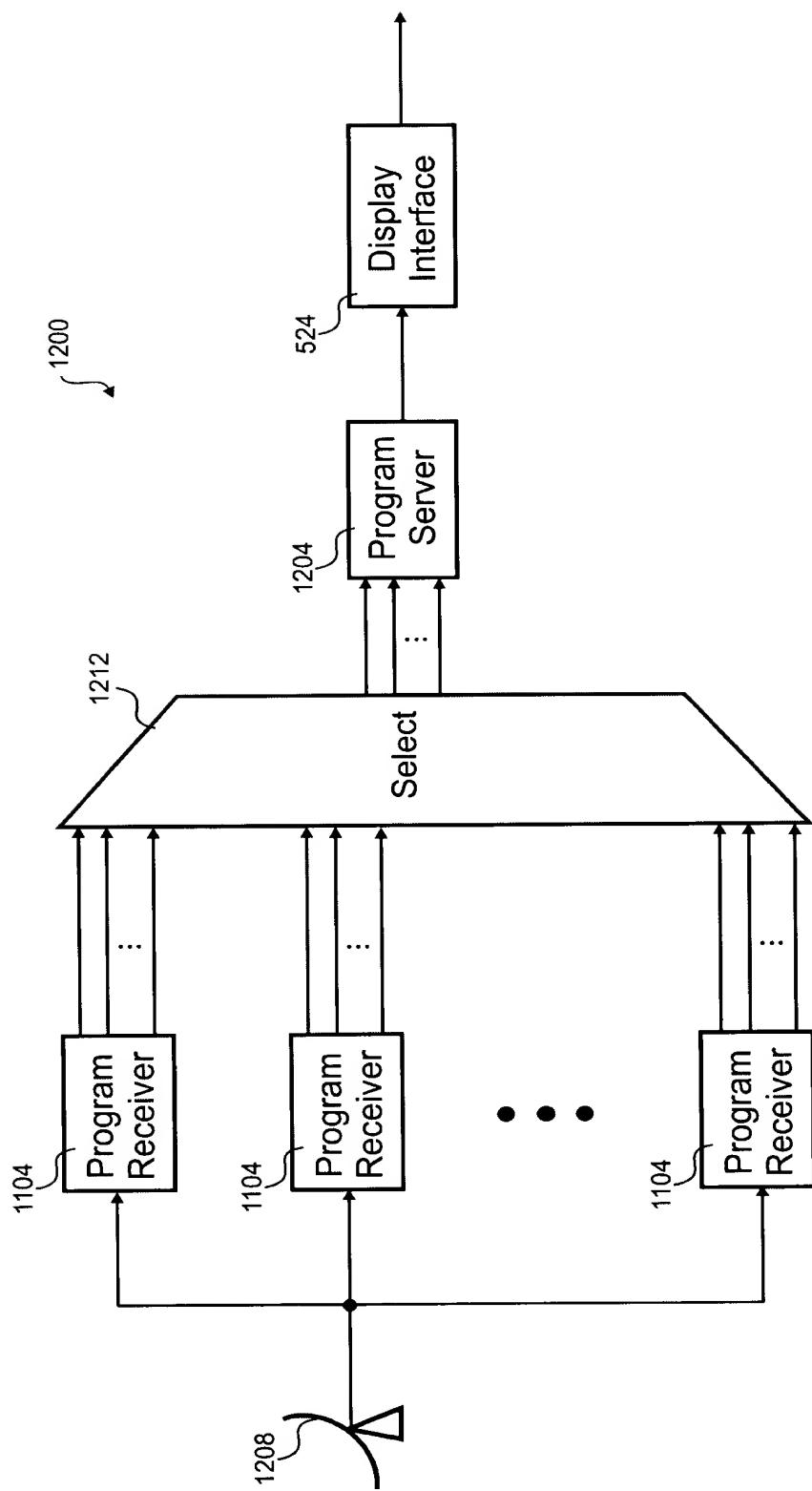


Fig. 12A

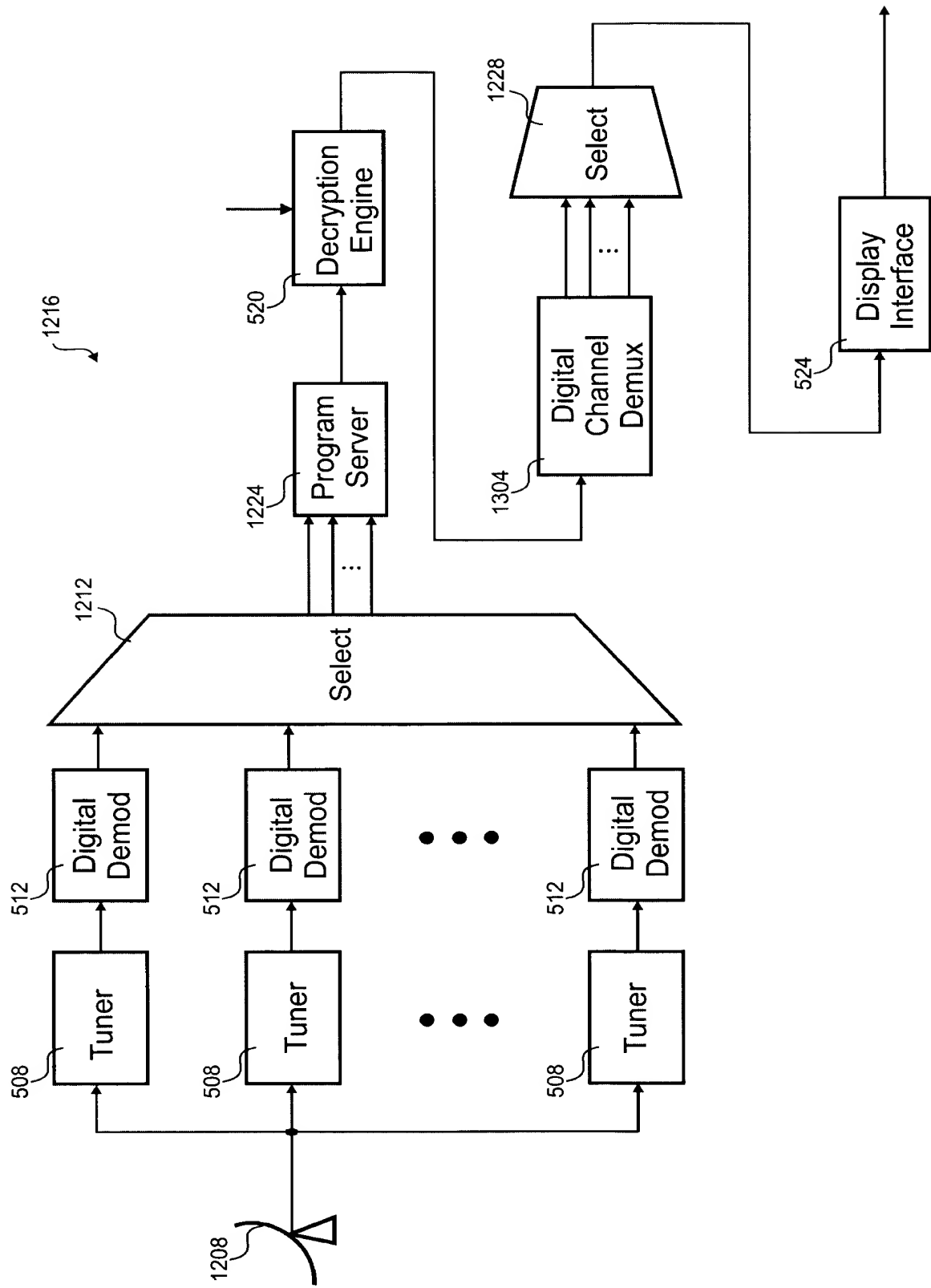


Fig. 12B

1104

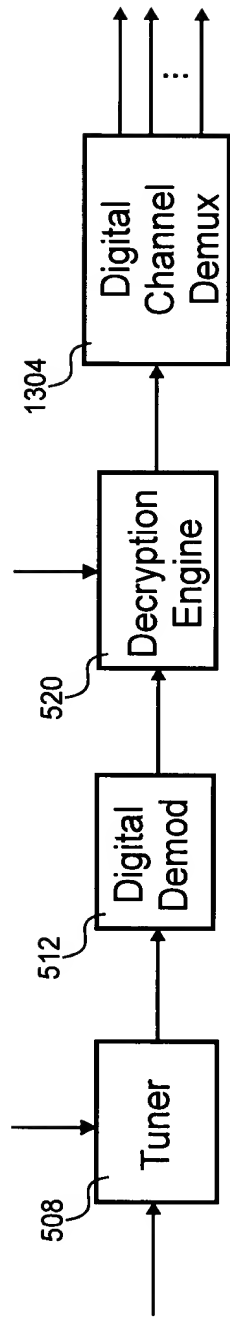


Fig. 13A

1104

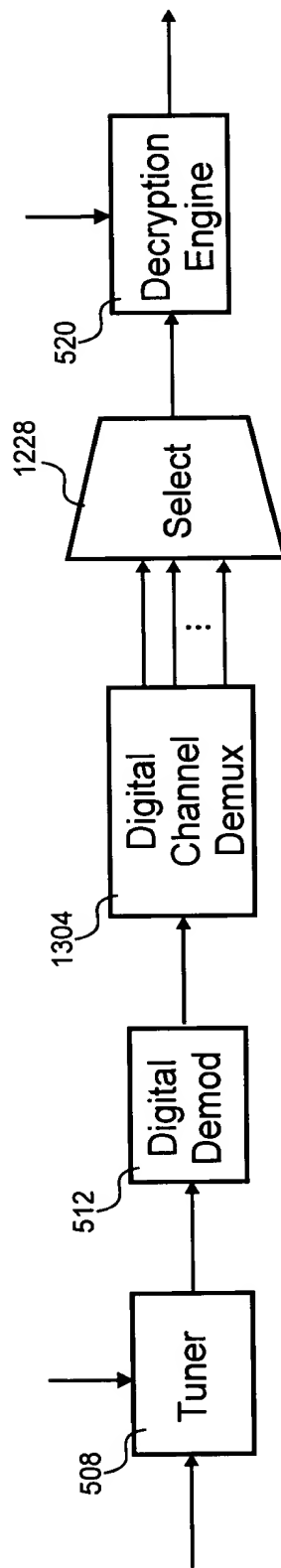


Fig. 13B

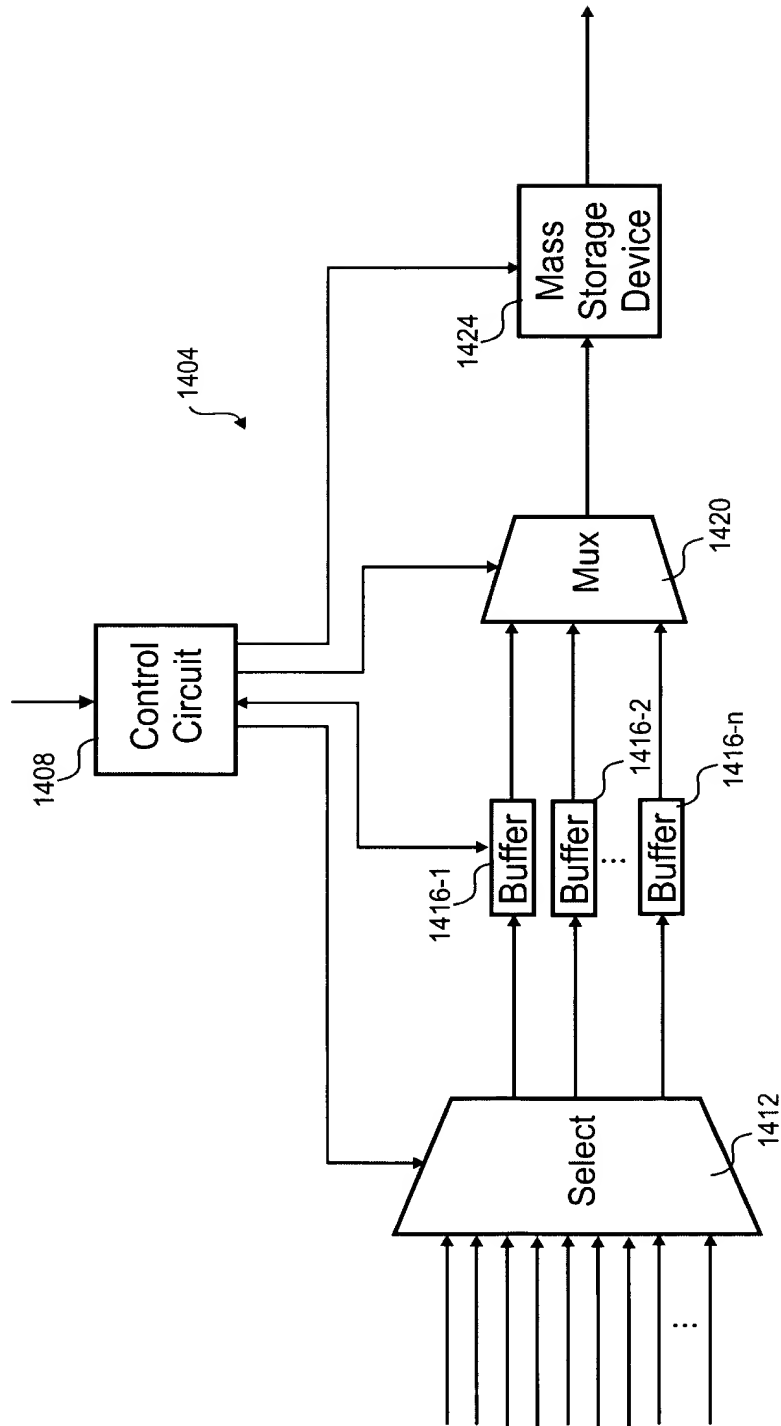


Fig. 14

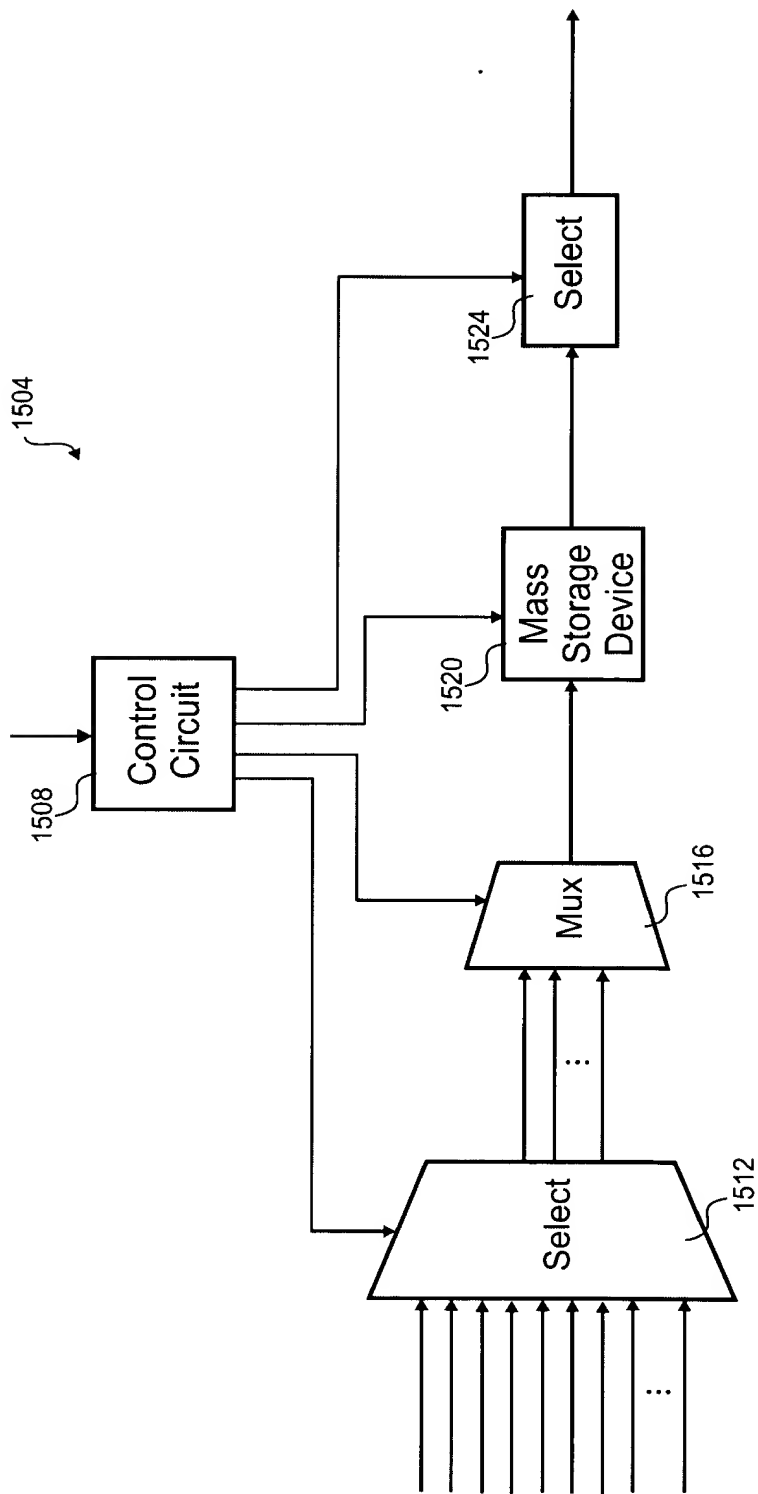


Fig. 15

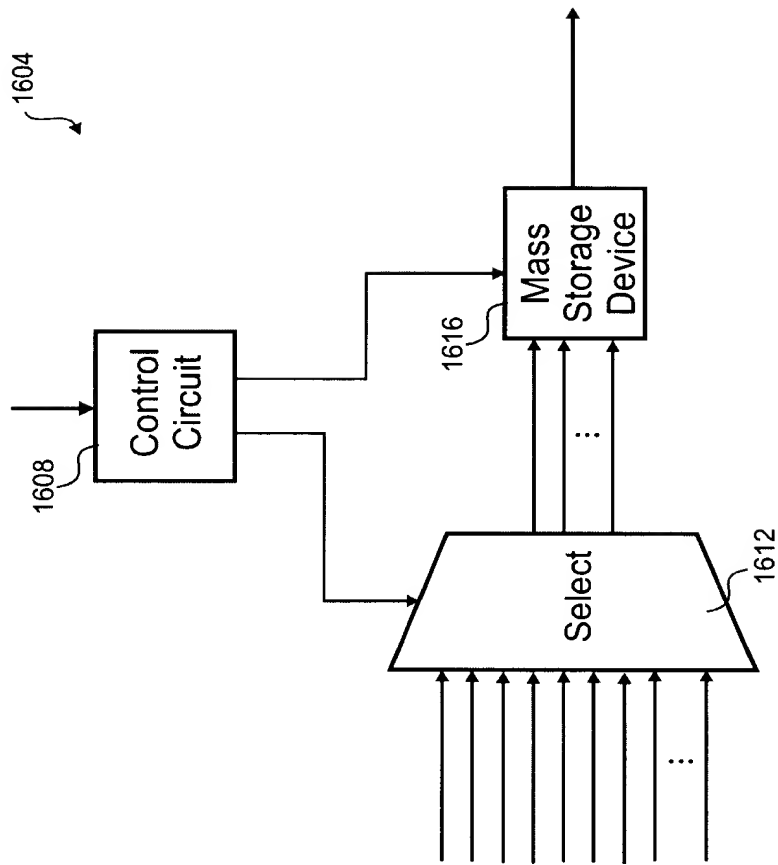


Fig. 16